

NA62 experiment

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- NA62 experiment
 - Physics
 - Detector
- Data acquisition system (DAQ)
- Trigger
- Conclusion

Kaon physics at CERN

Charge-Parity (CP) symmetry violation
 – NA31 (1986-89) - first evidence of direct CP violation
 – NA48 (1997-2001) – confirmation

The NA48 experiment at CERN has recently obtained the final result on the ϵ'/ϵ parameter, that quantifies direct CP violation in the $K^0 - \bar{K}^0$ system. The total data sample, acquired during five years of data-taking, corresponds to 5.3 million $K_L \rightarrow \pi^0 \pi^0$ decays. The result, $\text{Re}(\epsilon'/\epsilon) = (14.7 \pm 2.2) \times 10^{-4}$, confirms unambiguously the occurrence of direct CP violation. Converting this number in a precision test of the Standard Model picture of CP violation is an open challenge for non-perturbative QCD calculations. **Phys.Lett.B 544 (2002) 97-112**

NA62 search for new physics beyond Standard model

NA62 physics goals

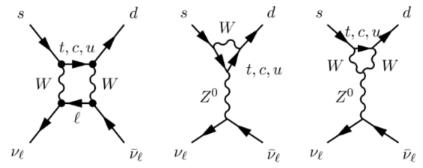
■ NA62 main physics goal: measure the BR of the ultra-rare decay K+ $\rightarrow \pi$ +vv with

~10% precision

- Theory Standard model:
 - well understood uncertainty < 10%
 The Standard model expectation is

BR(K+ $\rightarrow \pi + vv$) = (8.4 ± 1.0) x 10-11

- Sensitive to the new Physics



Trigger challenges

- Needs high intensity kaon beam \rightarrow 10⁺¹² Kaons
- Huge background (~ 11 orders of magnitude higher than signal) to be rejected
- Weak K+ →π+vv signature: difficult to trigger on signal

NA62 – fixed target experiment



Fixed target experiment at CERN SPS

Secondary 75 GeV/c hadron beam (6% kaons) produced by 400 GeV/c primary SPS protons impinging on a Beryllium target

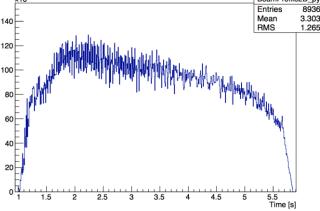
Beam structure

Very different environment wrt LHC:

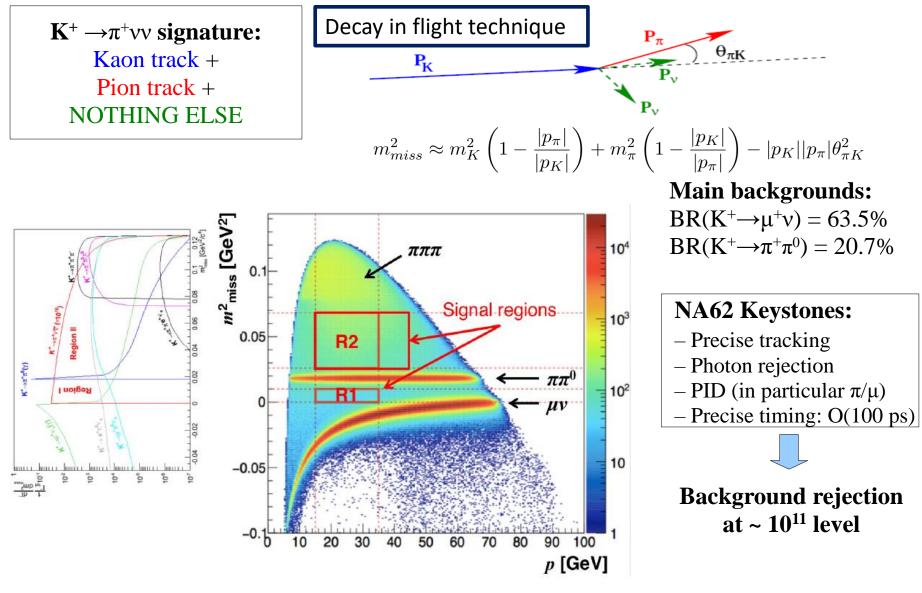
Continuous beam from SPS => bunched in spills of 4.8s

Asynchronous trigger: no bunch crossing! High-intensity hadron beam (~ 750 MHz!)

=> excellent time resolution
(<1ns) needed at trigger level!</pre>

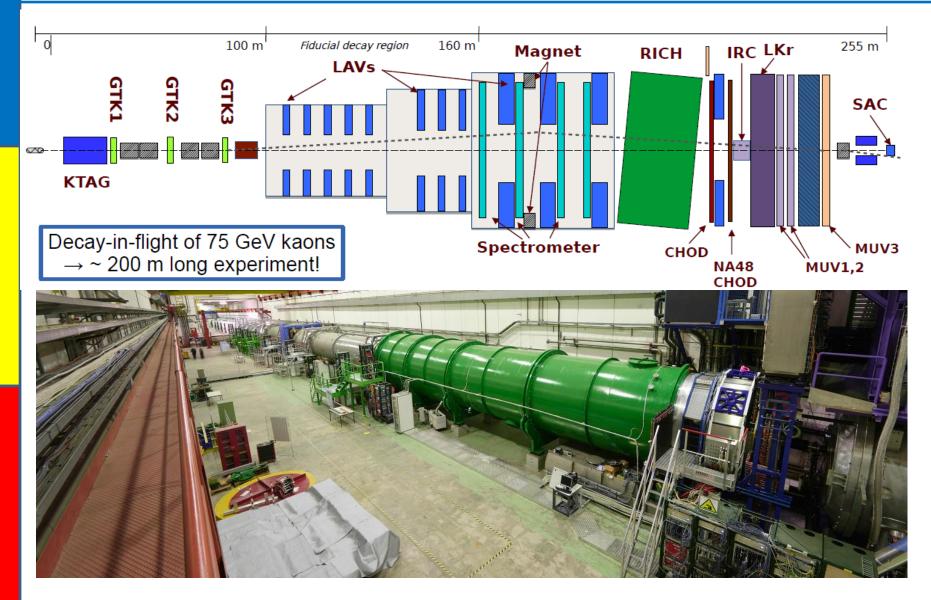


$K^+ \rightarrow \pi^+ vv$ at NA62: strategy

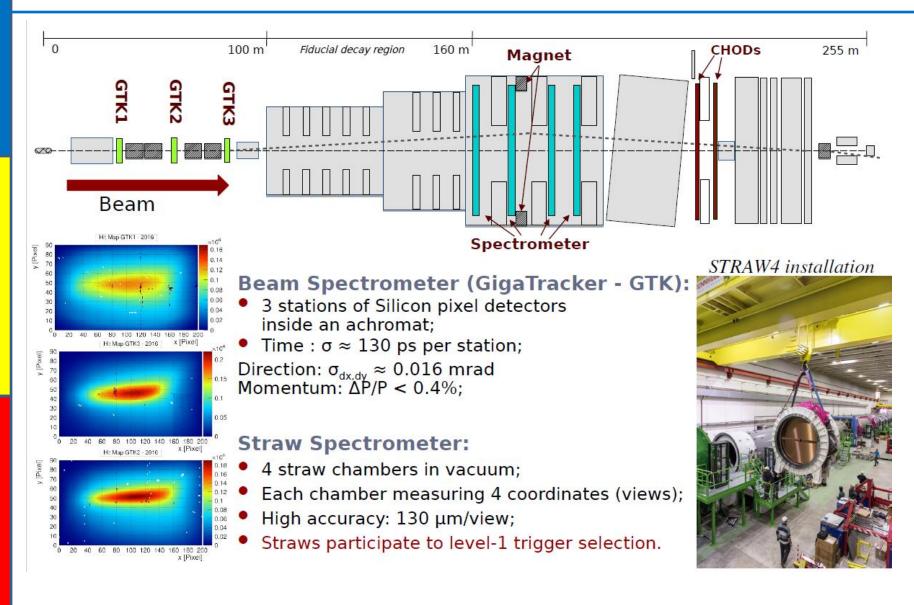


NA62 Trigger

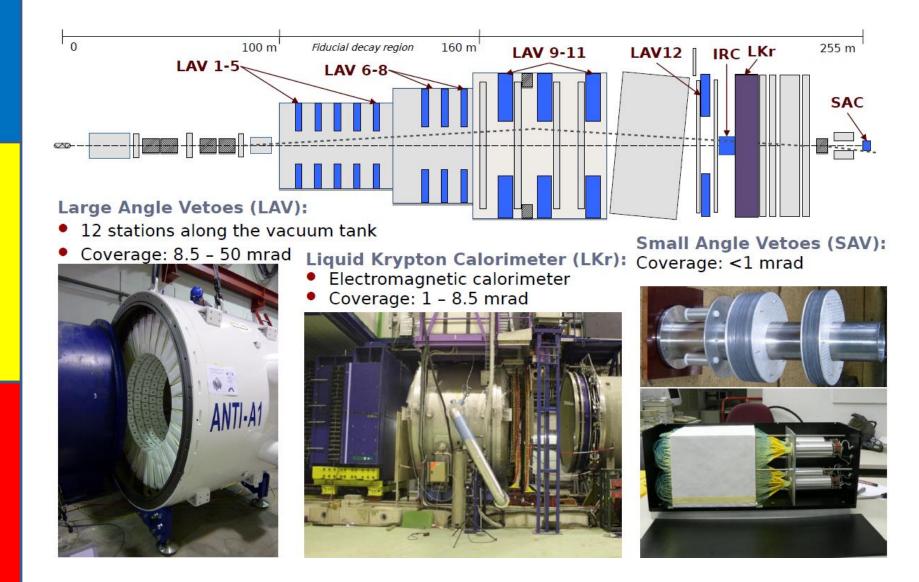
The NA62 Experiment



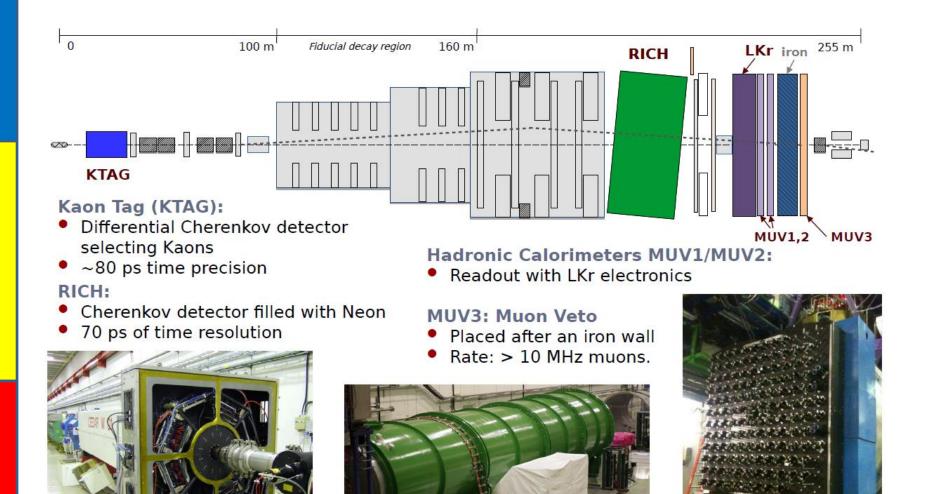
Tracking



Photon rejection



Particle ID



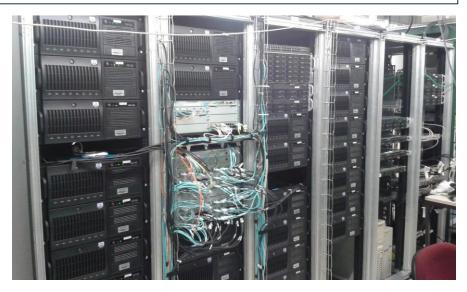
Trigger and DAQ system

- Two level trigger
 - Hardware L0 based on custom program electronics
 - Software L1running on a dedicated DAQ computing cluster

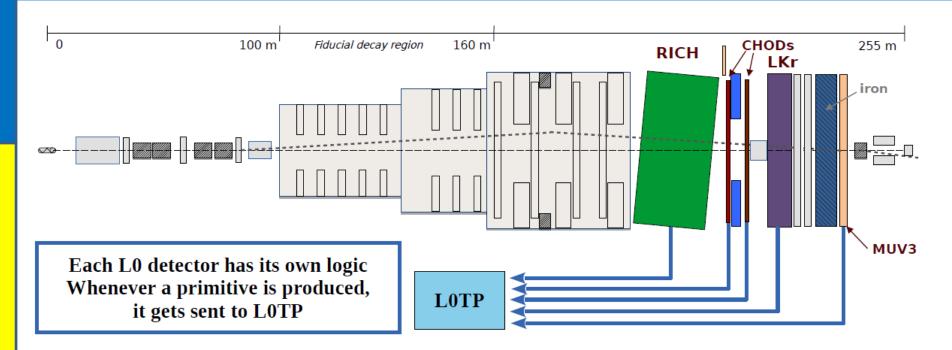
LO Trigger Processor (LOTP)



NA62 PC Farm: 30 PCs for HLT and event building + 4 mergers for raw data file building



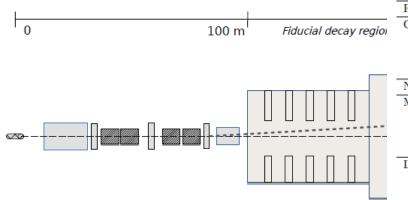
NA62 DAQ and Trigger



Detector used for L0 trigger:

- RICH Cerenkov for Particle ID pions wrt muons
- CHOD Charge Hodoscope
- NA48-CHOD
- MUV3 muon veto
- LKr Liquid krypton calorimeter (muon veto)

NA62 DAQ and Trigger



Each L0 detector has its own logic Whenever a primitive is produced, it gets sent to L0TP

Detector	Condition	Description		
RICH	RICH	At least two signals in the detector		
CHOD	Q1	At least one signal in any quadrant		
	Q2	At least one signal in each of two different quadrants		
	QX	At least one signal in each of two diagonally-opposite quadrants		
	UTMC	Upper multiplicity condition: fewer than five signals in the detector		
NA48-CHOD	NA48-CHOD	At least one signal in any quadrant		
MUV3	M1	At least one signal in the detector		
	MO1	At least one signal in the outer tiles		
	MO2	At least two signals in the outer tiles		
	MOQX	At least one signal in each of two diagonally-opposite quadrants		
LKr	E10	At least 10 GeV deposited in the LKr		
	E20	At least 20 GeV deposited in the LKr		
	E30	At least 30 GeV deposited in the LKr		
	C2E5	At least 5 GeV deposited in the LKr by at least two clusters		
	LKr30	Logical OR between E30 and C2E5		
LKr	E1	At least 1 GeV deposited in the LKr		
(beam dump)	E2	At least 2 GeV deposited in the LKr		
	E4	At least 4 GeV deposited in the LKr		
	C2E2	At least 2 GeV deposited in the LKr by at least two clusters		

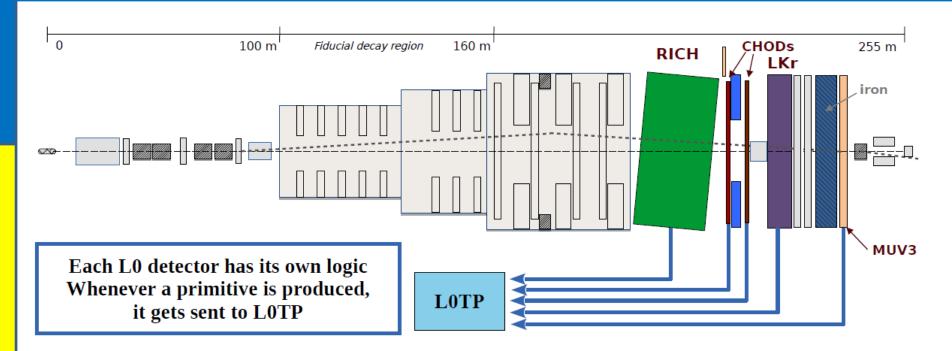
Table 1: List of L0 trigger conditions. The LKr conditions differ between K^+ and beam-dump configuration.

Detector used for L0 trigger:

- RICH Cerenkov for Particle ID pie
- CHOD Charge Hodoscope
- o NA48-CHOD
- MUV3 muon veto
- LKr Liquid krypton calorimeter (m

Trigger line	L0 trigger conditions	L1 trigger conditions
PNN	$RICH \cdot Q1 \cdot UTMC \cdot \overline{QX} \cdot \overline{M1} \cdot \overline{LKr30}$	$KTAG \cdot \overline{LAV} \cdot STRAW$
Non- μ	$RICH \cdot Q1 \cdot \overline{M1}$	$\rm KTAG\cdot STRAW-1TRK$
MT	$RICH \cdot QX$	$KTAG \cdot STRAW-Exo$
$2\mu \mathrm{MT}$	$RICH \cdot QX \cdot MO2$	$KTAG \cdot STRAW-Exo$
$e \mathrm{MT}$	$RICH \cdot QX \cdot E20$	KTAG · STRAW-Exo
$\mu \mathrm{MT}$	$RICH \cdot QX \cdot MO1 \cdot E10$	$\mathrm{KTAG} \cdot \overline{\mathrm{LAV}} \cdot \mathrm{STRAW}\text{-}\mathrm{MT}$
$DV-\mu$	$\rm RICH\cdot Q2\cdot MO1\cdot E10$	$\overline{\text{KTAG}} \cdot \text{STRAW-DV}$
$DV-2\mu$	$RICH \cdot Q2 \cdot MO2 \cdot \overline{E10}$	STRAW-DV
Neutrino	$RICH \cdot Q1 \cdot MOQX \cdot \overline{Q2}$	$\mathrm{KTAG} \cdot \overline{\mathrm{LAV}} \cdot \mathrm{STRAW}\text{-}1\mathrm{TRK}$
Control	NA48-CHOD	None

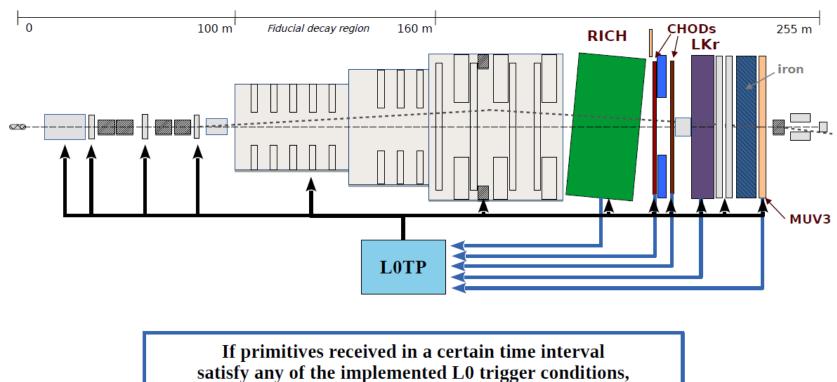
NA62 DAQ and Trigger



Trigger inputs are called LO Primitives (64 bits):

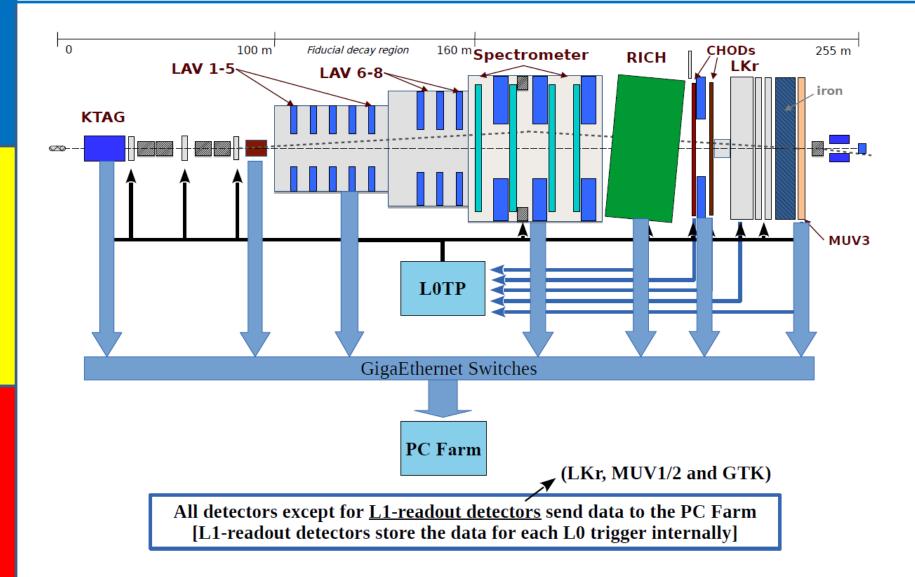
• Timestamp (32) bits – 25 ns resolution - 40 MHz clock is used using LHC TTC system and ALICE like Local Trigger Unit

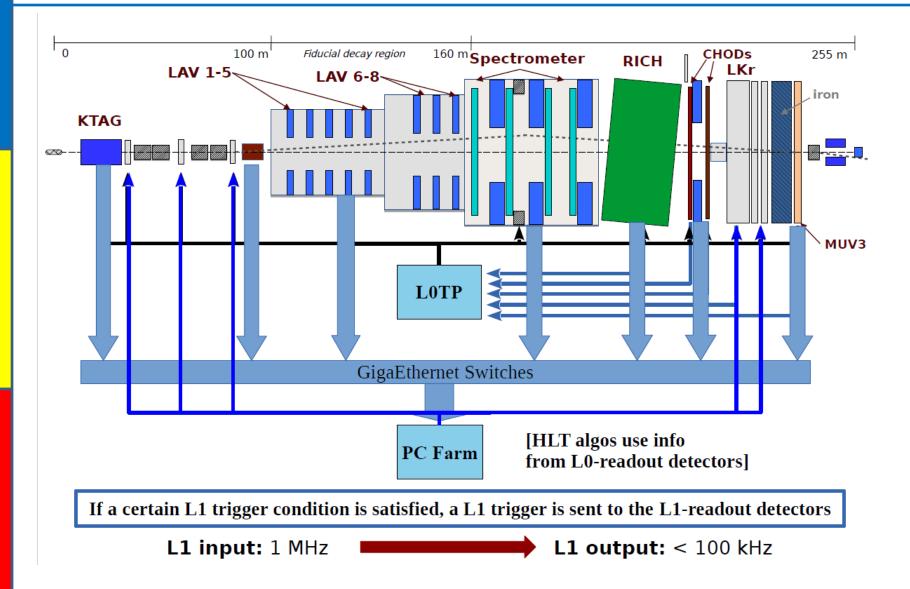
- Fine time (8 bits) 0.1 ns resolution synchronised with 40 MHz clock
- Primitive ID (16 bits) detector information (energy, multiplicity, position)

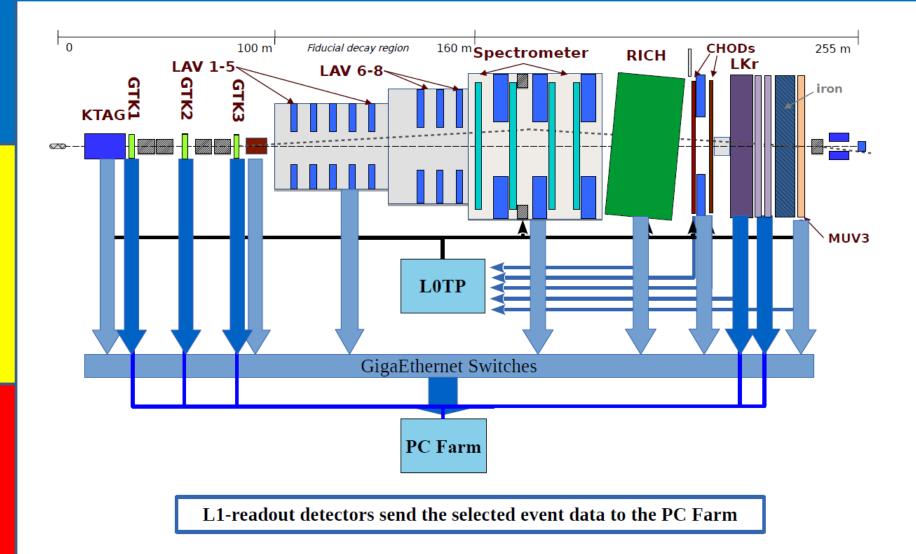


a L0 trigger is produced by L0TP and is sent to ALL detectors [Maximum latency: 1 ms]

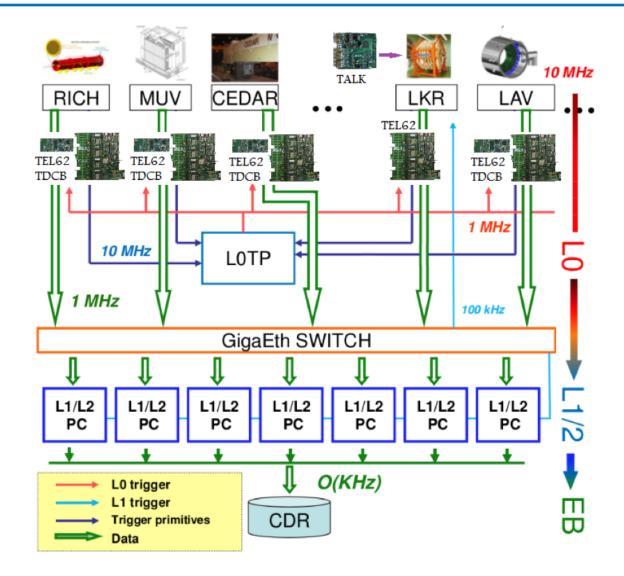
LO input: 10 MHz LO output: 1 MHz







NA62 TDAQ schema



Number of trigger per spill

Trigger line	Downscaling	L0 triggers [10 ³]	L1 triggers [10 ³]
PNN	1	1540	74
Non- μ	200	30	12
MT	100	39	4
$2\mu MT$	2	150	30
eMT	8	193	22
μ MT	5	99	10
$DV-\mu$	5	140	0.3
DV-2 μ	3	160	5
Neutrino	15	10	3
Control	400	94	94
Total:	_	2455	254.3
		*	

[counts measured at 60% beam intensity]

Corresponds to $\sim 1~MHz~L0$ and $\sim 100~kHz~L1$ at 100% beam intensity

Trigger efficiency

Efficiency evaluated using minimum bias data

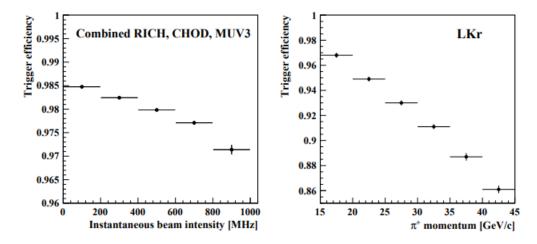


Figure 4: Efficiency of the combined RICH, CHOD, and MUV3 conditions of the PNN trigger line evaluated using $K^+ \to \pi^+\pi^0$ events as a function of instantaneous beam intensity (left), and efficiency of the LKr condition of the PNN trigger line evaluated using $K^+ \to \pi^+\pi^0$ events as a function of the π^+ momentum (right).

Summary

- Good time resolution is essential already at the level-0 trigger in order to reject the high background.
- All the systems involved in the trigger have a level-0 time-resolution < 2.5 ns.
- NA62 is able to handle ~10 MHz of decay-products, reducing the rate to 1 MHz after level-0 and to 100 kHz after level-1 triggers.
 - Multiple trigger logic conditions to perform a broad physics programme